

Fig. 1

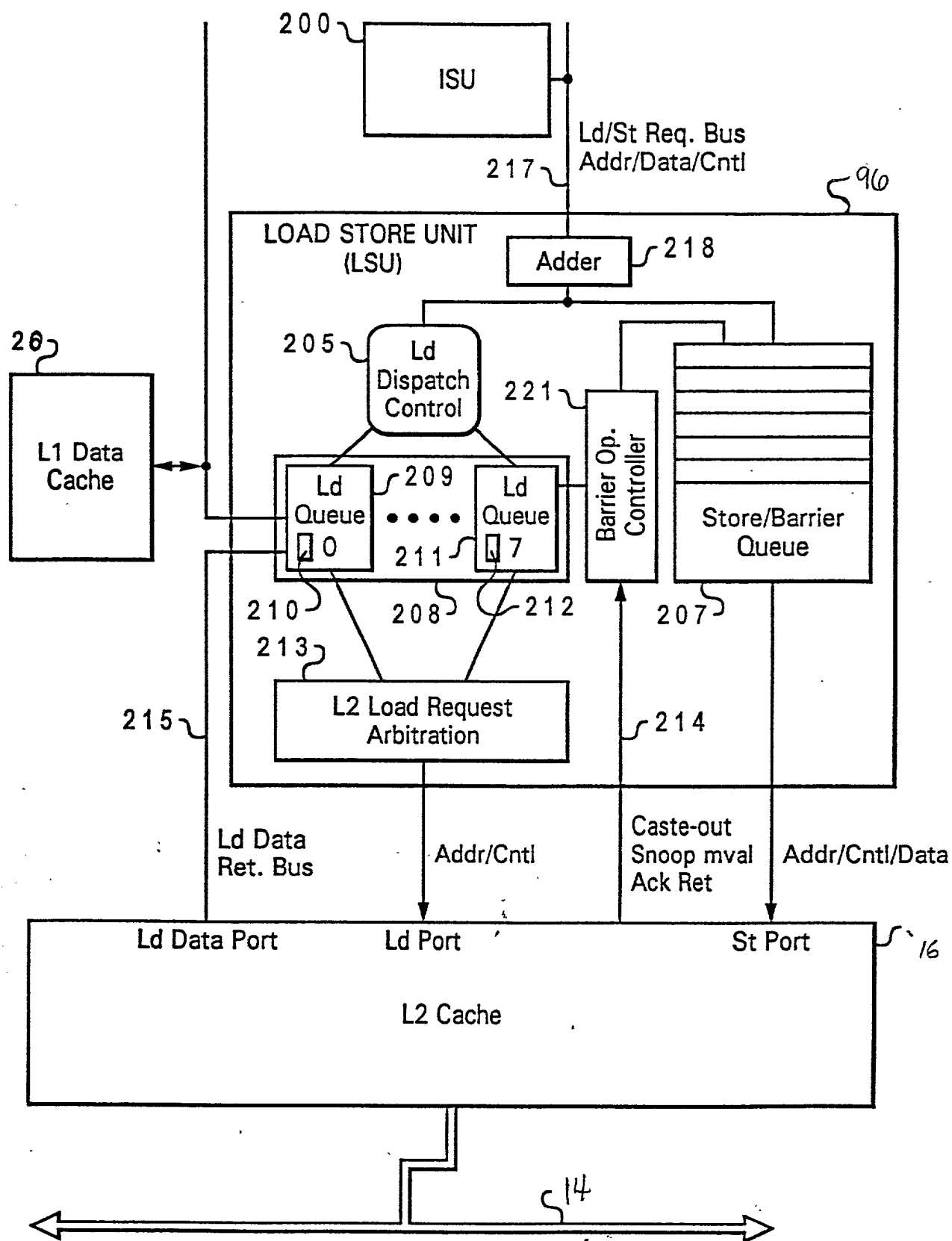


Fig. 2

FIG. 3

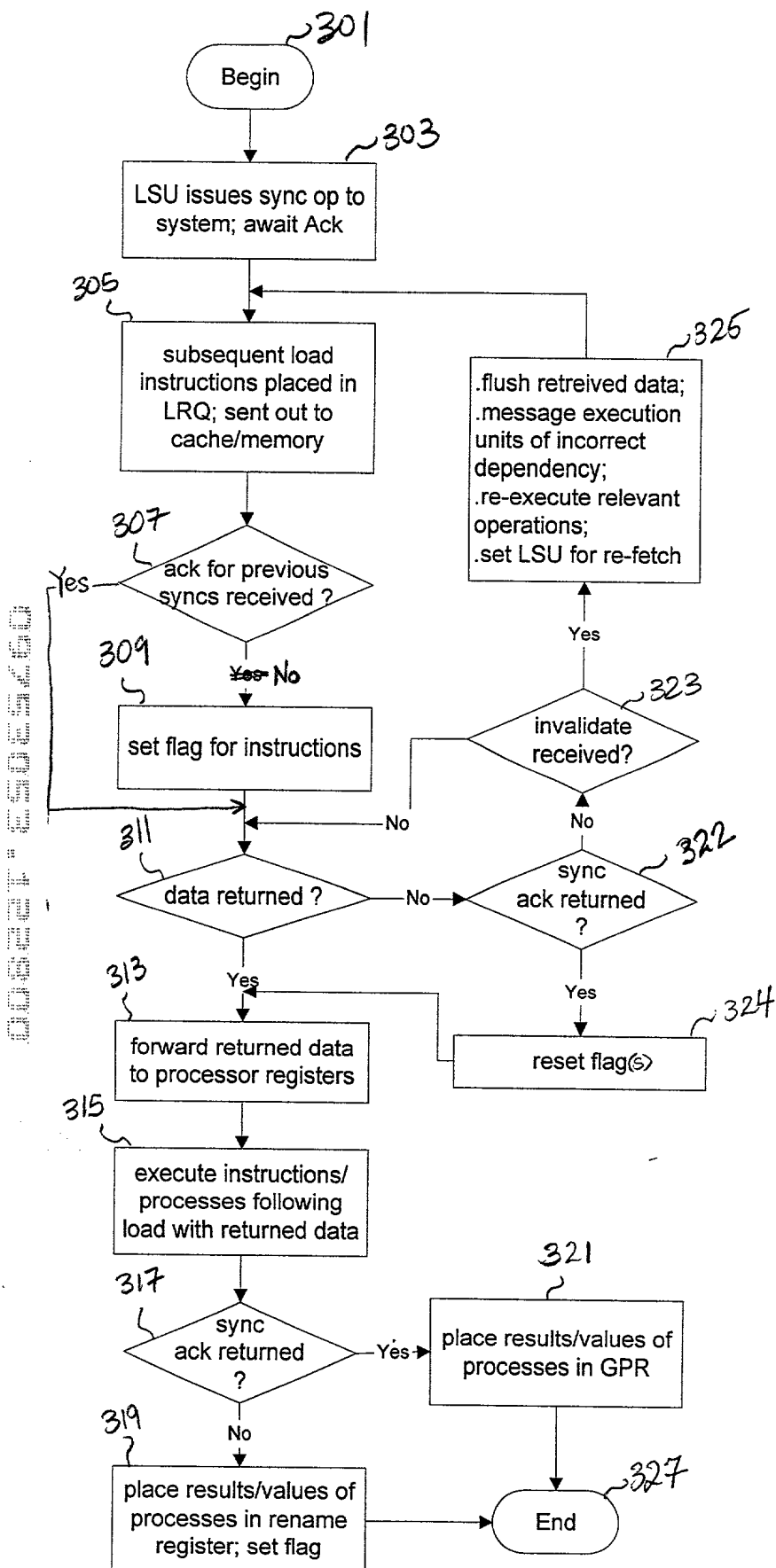


Figure 4

No.	Flag	Register rename entries (values)	GPR #
1	0		XXXXX0
2	1		XXX11
3	1		XXX10
.			
.			
.			
80	0		XX1XX

500

The diagram illustrates a sequence of transactions on a 28-bit bus. The horizontal axis represents time, divided into 28-bit slots numbered 0 to 281. The vertical axis represents the bus lines, labeled 0A through 7. Transactions are indicated by horizontal arrows with labels above or below them. Snoop transactions are labeled 'snoop' and 'resp' (response). Data transactions are labeled 'Ddata', 'Bdata', and 'Qdata'. Acknowledgments are labeled 'Ack C', 'Ack P', and 'Ack Q'. The sequence of events is as follows:

- 0A:** snoop (at bit 25), resp (at bit 26)
- 2B:** snoop (at bit 25), resp (at bit 26), Bdata (at bit 150), EGJK (at bits 151-154)
- 3D:** Ddata (at bit 14), L (at bit 153)
- 4:** F (at bit 17), C (at bit 25), Ack C (at bit 126), N (at bit 155), snoop (at bit 180), resp (at bit 181)
- 5:** M (at bit 17), I (at bit 126), O (at bit 155), snoop (at bit 180), resp (at bit 181), P (at bit 181), Ack P (at bit 281)
- 6Q:** Qdata (at bit 14), H (at bit 126)
- 7:** R ... (at bit 14)